

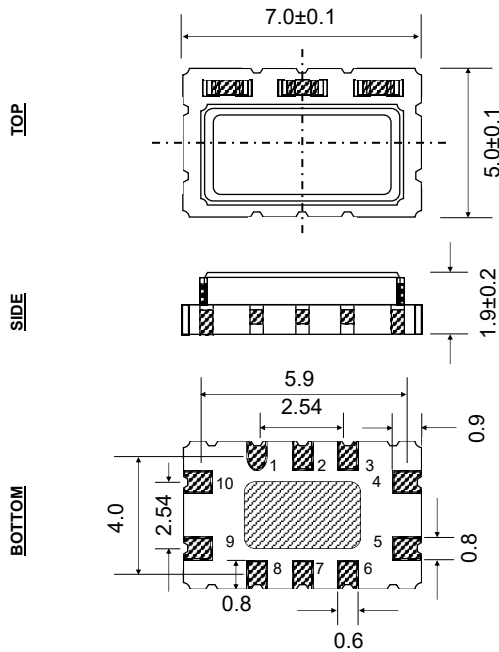
STRATUM III MINIATURE CERAMIC SMD TCXO / VCTCXO (7.0 x 5.0 x 1.9mm)
FEATURES

- STRATUM III APPLICATION
- ± 4.6 ppm OVERALL PERFORMANCE INCLUDING 20 YEAR AGING
- CMOS or CLIPPED SINEWAVE OUTPUT OPTION


■ SPECIFICATION

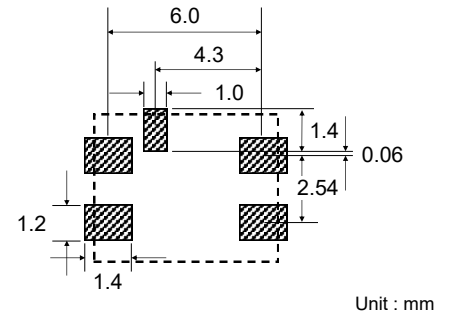
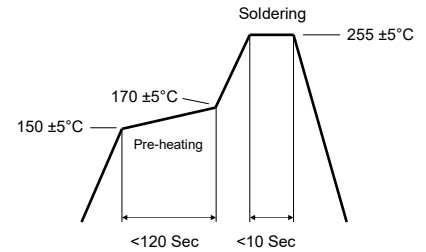
PARAMETER		MIN.	TYP.	MAX.	UNIT	NOTE	
FREQUENCY RANGE		5		26	MHz		
STANDARD FREQUENCIES		CMOS					8.192M, 10M, 12.8M, 20M
		CSW					8.192M, 10M, 12.8M, 16.384M, 19.2M, 19.44M, 20M, 25M, 26M
FREQUENCY OVERALL STABILITY				± 4.6	ppm	20Years including calibration	
FREQUENCY STABILITY	vs. TEMPERATURE RANGE			± 0.28	ppm	See P/N guide for other options	
	vs. LOAD $\pm 10\%$			± 0.2	ppm		
	vs. SUPPLY VOLTAGE $\pm 5\%$			± 0.2	ppm		
	vs. AGING			± 1.0	ppm/year	@ 25°C	
OPERATING TEMPERATURE RANGE		-40		85	°C	See P/N guide for other options	
STORAGE TEMPERATURE RANGE		-55		125	°C		
SUPPLY VOLTAGE $\pm 5\%$		3.135	3.3	3.165	V		
SUPPLY CURRENT		CMOS					6.0 mA
		CSW					3.5 mA
OUTPUT	LOGIC FAMILY		CMOS / Clipped Sine Wave				
	LOAD	CMOS		15pF			
		CSW		10k Ω //10pF			
	LEVEL	CMOS (V _{OH})		0.9 x V _{DD}		V	
		CMOS (V _{OL})			0.1 x V _{DD}	V	
		CSW		0.8		V _{P-P}	
	SYMMETRY (DUTY CYCLE)		45		55	%	CMOS only
	RISE AND FALL TIME (Tr/Tf)				10	nS	CMOS only
START-UP TIME				2	mS		
STAND-BY VOLTAGE	ENABLE (V _{IH})		0.7 x V _{DD}		V		
	DISABLE (V _{IL})			0.3 x V _{DD}	V		
FREQUENCY DEVIATION		± 5		± 8	ppm		
CONTROL VOLTAGE		0.5		2.5	V		
INPUT IMPEDANCE		0.1			M Ω		
PHASE NOISE	100Hz			-120	dBc/Hz	@ 12.8MHz	
	1kHz			-140	dBc/Hz		
	10kHz			-148	dBc/Hz		

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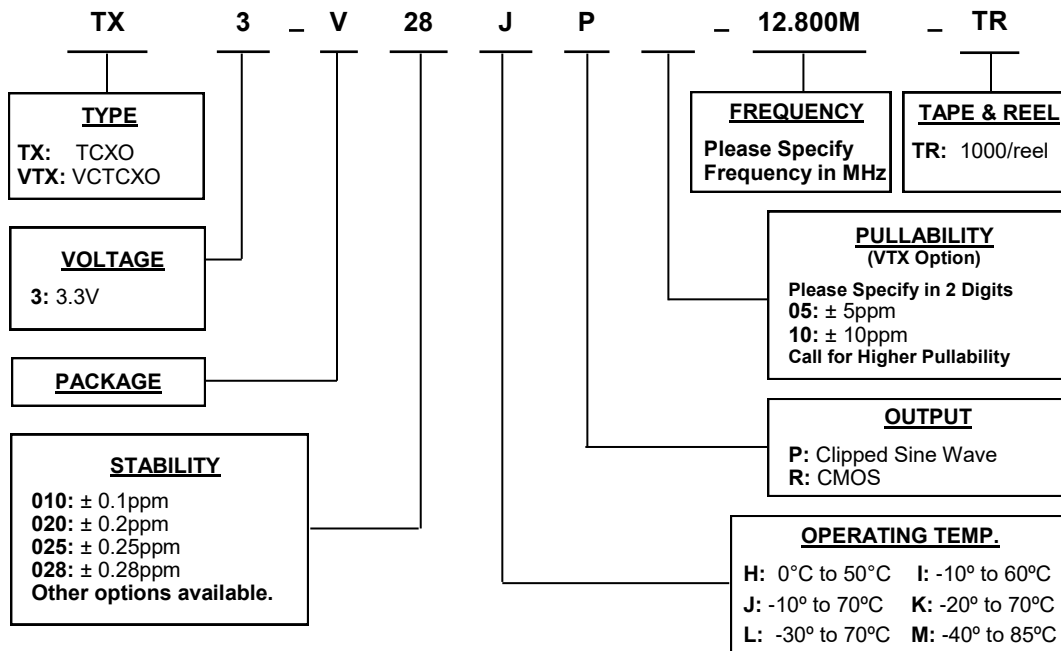
PACKAGE DIMENSIONS


Unit : mm

Pin Configuration	
1,2,3,6,7	N/C
9	V _{DD}
4	GND
5	Output
8	Tristate
10	V _c or GND

SOLDER PATTERN

REFLOW PROFILE


Note: Consult factory for detail

PART NUMBERING GUIDE


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