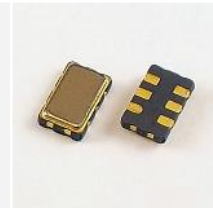


## CERAMIC LVPECL MINIATURE SMD OSCILLATOR (5.0 x 3.2 x 1.3mm)

### FEATURES:

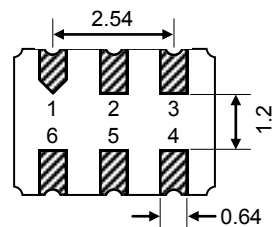
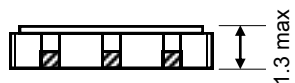
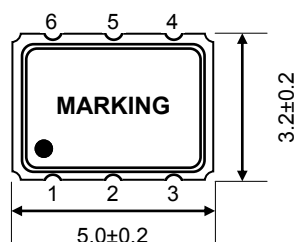
- WIDE FREQUENCY RANGE
- LOW JITTER
- EN/DISABLE PIN AVAILABLE



### ■ SPECIFICATION

<b>FREQUENCY RANGE</b>		0.750MHz ~ 800.000MHz	
<b>FREQUENCY STABILITY</b>		STD: ±50ppm, ±10ppm ~ ±100ppm max. (See P/N guide for other options)	
<b>OPERATING TEMPERATURE RANGE</b>		STD: 0°C ~ 70°C, option: -40°C ~ 85°C (See P/N guide for other options)	
<b>STORAGE TEMPERATURE RANGE</b>		-55°C ~ 125°C	
<b>SUPPLY VOLTAGE ± 10%</b>		2.5V <sub>DC</sub>	3.3V <sub>DC</sub>
<b>SUPPLY CURRENT</b>		70mA max.	90mA max.
<b>OUTPUT</b>	<b>LOGIC FAMILY</b>	LVPECL	
	<b>LOAD</b>	50Ω (into V <sub>DD</sub> - 2.0V <sub>DC</sub> )	
	<b>LEVEL LOGIC '1' (V<sub>OH</sub>)</b>	V <sub>DD</sub> - 1.025V <sub>DC</sub> min.	
	<b>LEVEL LOGIC '0' (V<sub>OL</sub>)</b>	V <sub>DD</sub> - 1.620V <sub>DC</sub> max.	
	<b>PEAK TO PEAK</b>	600mV min. 800mV typ.	
	<b>SYMMETRY(DUTY CYCLE)</b>	STD: 40/60%, Option: 45/55% (See P/N guide for other options)	
	<b>RISE AND FALL TIME (Tr/Tf)</b>	250pS typ. 1nS max.	
	<b>START UP TIME</b>	5mS max.	
<b>STAND-BY VOLTAGE</b>	<b>ENABLE (V<sub>IH</sub>)</b>	0.7 x V <sub>DD</sub> ≤ V <sub>IH</sub> ≤ V <sub>DD</sub> or N/C	
	<b>DISABLE (V<sub>IL</sub>)</b>	V <sub>IL</sub> ≤ 0.3 x V <sub>DD</sub>	
<b>AGING @ +25°C ±3°C</b>		±3.0ppm / 1 <sup>st</sup> year max.	
<b>PHASE JITTER (12kHz ~ 20MHz)</b>		1.0pS max.	

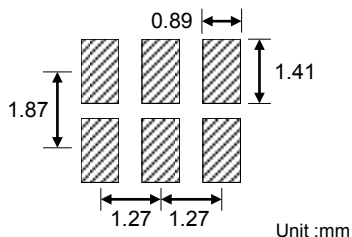
### ■ PACKAGE DIMENSIONS



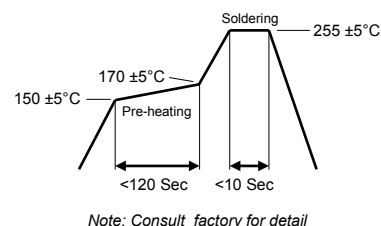
Unit : mm

Pin Configuration	
1	En/Dis or N.C.
2	N.C.
3	GND
4	Output
5	C - output
6	V <sub>DD</sub>

### ■ SOLDER PATTERN

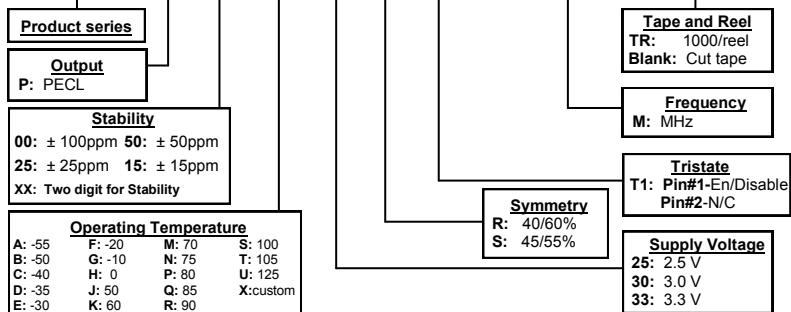


### ■ REFLOW PROFILE



### ■ PART NUMBERING GUIDE

TPSM5 - P 50 HM 33 S T1 - 148.500M - TR



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