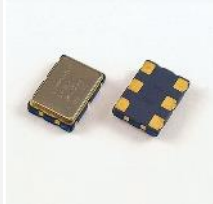
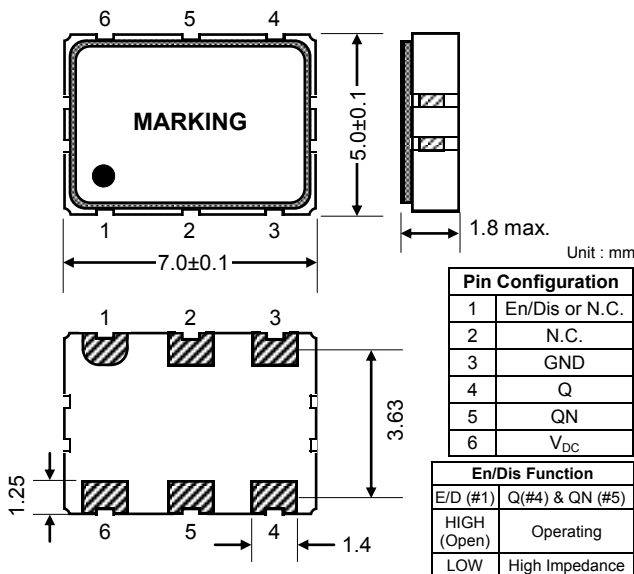
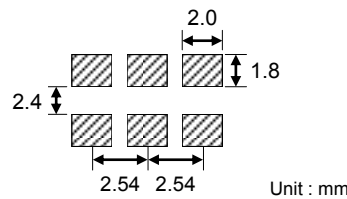
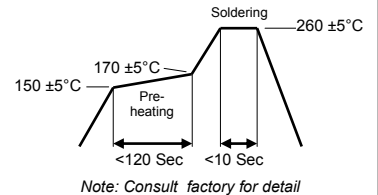
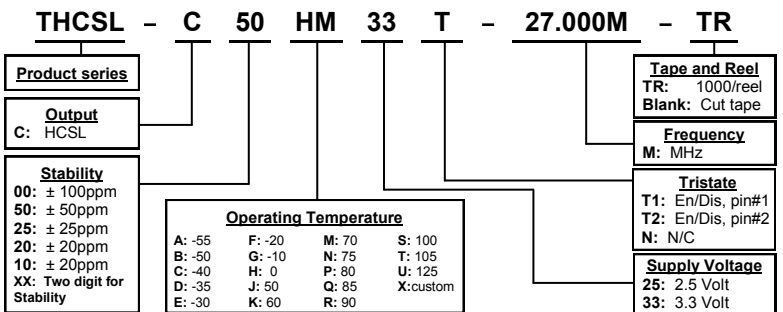


**CERAMIC SMD HCSL OUTPUT OSCILLATOR (7.0 x 5.0 x 1.8mm)**
**FEATURES:**

- LOW PHASE JITTER (12kHz TO 20MHz) UNDER 1pS (RMS)
- RoHS COMPLIANT
- LOW PROFILE PACKAGE DESIGN (1.8MM HEIGHT MAX.)


**■ SPECIFICATION**

<b>FREQUENCY RANGE</b>		13.500MHz ~ 200.000MHz	
<b>FREQUENCY STABILITY</b>		±25ppm ~ ±100ppm	
<b>OPERATING TEMPERATURE RANGE</b>		STD: -10°C ~ 70°C, Option: -40°C ~ 85°C (See P/N guide for other options)	
<b>STORAGE TEMPERATURE RANGE</b>		-55°C ~ 125°C	
<b>SUPPLY VOLTAGE ± 5%</b>		2.5V <sub>DC</sub>	3.3V <sub>DC</sub>
<b>SUPPLY CURRENT</b>	f < 90MHz	17mA typ. 27mA max.	
	90MHz ≤ f < 125MHz	18mA typ. 28mA max.	
	125MHz ≤ f < 160MHz	19mA typ. 29mA max.	
	160MHz ≤ f	30mA typ. 50mA max.	
<b>OUTPUT</b>	<b>LOGIC FAMILY</b>	HCSL square wave	
	<b>LOAD</b>	50Ω (Between output and GND)	
	<b>LEVEL LOGIC '1' (V<sub>OH</sub>)</b>	0.55V ≤ V <sub>OH</sub> ≤ 0.8V	0.6V ≤ V <sub>OH</sub> ≤ 0.85V
	<b>LEVEL LOGIC '0' (V<sub>OL</sub>)</b>	-0.16V ≤ V <sub>OL</sub> ≤ 0.15V	-0.16V ≤ V <sub>OL</sub> ≤ 0.15V
	<b>PEAK TO PEAK @ V<sub>DD</sub>= 2.5V</b>	0.6V typ.	0.65V typ.
	<b>SYMMETRY(DUTY CYCLE)</b>	45% / 55%	
	<b>RISE AND FALL TIME (Tr/Tf)</b>	0.15nS typ. 0.7nS max.	
	<b>START-UP TIME</b>	3mS typ. 10mS max.	
<b>STAND-BY VOLTAGE</b>	<b>ENABLE (V<sub>IH</sub>)</b>	2.4V ≤ V <sub>IH</sub> ≤ V <sub>DC</sub> OR N.C.	
	<b>DISABLE (V<sub>IL</sub>)</b>	V <sub>IL</sub> ≤ 0.6V	
<b>STAND-BY CURRENT</b>		10μA	
<b>AGING @ 25°C ± 3°C</b>		±3.0ppm / 1 <sup>st</sup> year max.	
<b>PHASE JITTER RMS (12KHZ ~ 20MHz)</b>		200fS typ.	
<b>PHASE NOISE @ (125MHz OFFSET)</b>		-50dBc/Hz @ 10Hz -82dBc/Hz @ 100Hz -116dBc/Hz @ 1kHz -138dBc/Hz @ 10kHz	-144dBc/Hz @ 100kHz -149dBc/Hz @ 1MHz -155dBc/Hz @ 10MHz

**■ PACKAGE DIMENSIONS**

**■ SOLDER PATTERN**

**■ REFLOW PROFILE**

**■ PART NUMBERING GUIDE**


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