

## STANDARD THROUGH HOLE VCXO (20.7 x 13.1 x 7.5mm)

### FEATURES

- WIDE FREQUENCY RANGE
- WIDE PULLABILITY RANGE / LOW CURRENT
- CMOS / TTL / SINEWAVE OUTPUT
- FAST DELIVERY

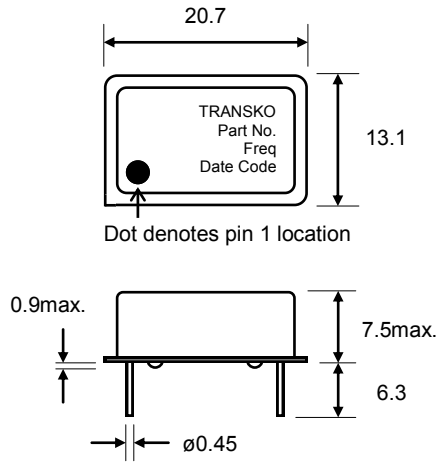


### ■ SPECIFICATION

PARAMETER		MIN.	TYP.	MAX.	UNIT	NOTE	
FREQUENCY RANGE	CMOS / TTL	1K		800M	Hz		
	SW-14Pins	6M		190M	Hz		
	SW-8Pins	10M		50M	Hz		
FREQUENCY STABILITY		±10		±50	ppm		
OPERATING TEMPERATURE RANGE		-40		85	°C	See P/N guide for other options	
STORAGE TEMPERATURE RANGE		-55		105	°C		
SUPPLY VOLTAGE ±5%	V <sub>DD</sub> = 3.3V <sub>DC</sub>	3.135	3.300	3.465	V	See P/N guide for other options	
	V <sub>DD</sub> = 5.0V <sub>DC</sub>	4.750	5.000	5.250	V		
SUPPLY CURRENT	CMOS			100	mA	(V <sub>DD</sub> = 3.3V <sub>DC</sub> , 5.0V <sub>DC</sub> )	
	TTL			100	mA		
	SINEWAVE			70	mA		
OUTPUT	LOAD	CMOS		15	pF		
		TTL		10	TTL		
		SINEWAVE		50	Ω	Output - GND	
	LEVEL	CMOS (V <sub>OH</sub> )	0.9 x V <sub>DD</sub>			V	
		CMOS (V <sub>OL</sub> )			0.1 x V <sub>DD</sub>	V	
		TTL (V <sub>OH</sub> )	2.4			V	
		TTL (V <sub>OL</sub> )			0.4	V	
		SINEWAVE (3.3V)		0		dBm	
		SINEWAVE (5.0V)		10		dBm	
	SYMMETRY (DUTY CYCLE)	CMOS	40		60	%	
		TTL	40		60	%	
		SINEWAVE	-	-	-		
	RISE AND FALL TIME (Tr/Tf)	CMOS			10	nS	
		TTL			10	nS	
		SINEWAVE	-	-	-		
	START-UP TIME				10	mS	
	FREQUENCY DEVIATION		50			ppm	See P/N guide for other options
	CONTROL VOLTAGE	V <sub>DD</sub> = 3.3V <sub>DC</sub>	0.3		3.0	V	
V <sub>DD</sub> = 5.0V <sub>DC</sub>		0.5		4.5	V		
AGING				± 3.0	ppm/year	@ 25°C ± 3°C	
PHASE JITTER RMS				1.0	pS	@ 12kHz ~ 20MHz	

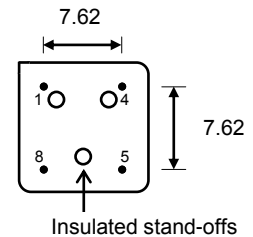
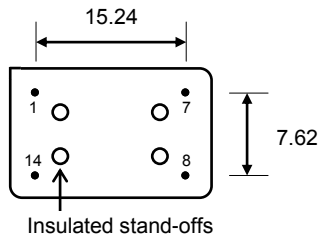
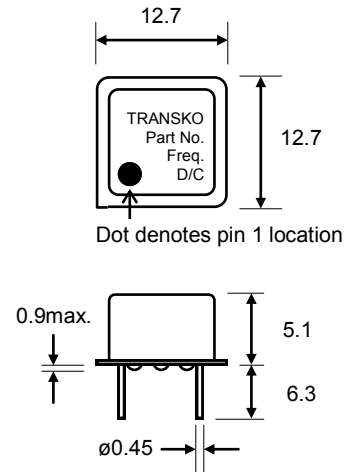
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## ■ PACKAGE DIMENSIONS

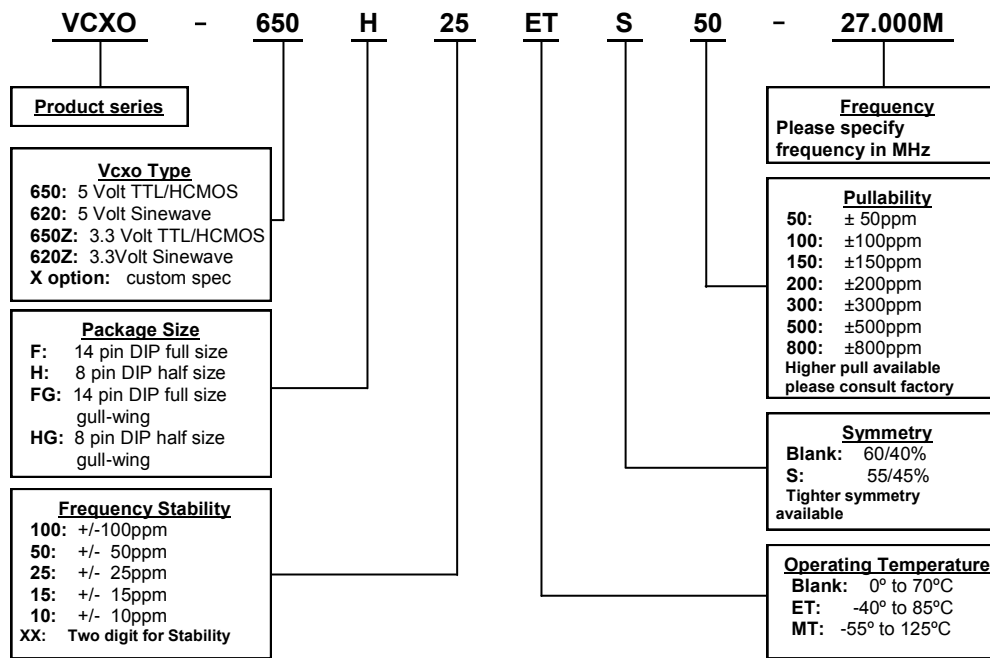


Pin Configuration		
14 pin		8 pin
1	V Control	1
7	GND	4
8	Output	5
14	V <sub>DD</sub>	8

Unit : mm



## ■ PART NUMBERING GUIDE



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