

FR-4 SMD LVDS OUTPUT OSCILLATOR (14.3 x 8.7 x 5.5mm)

FEATURES:

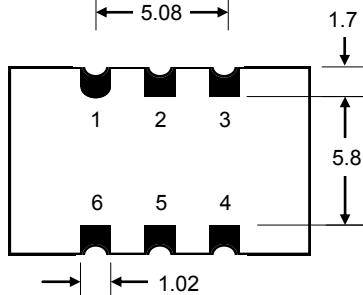
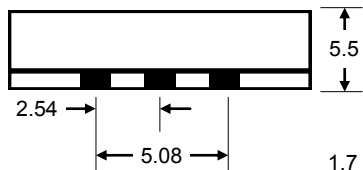
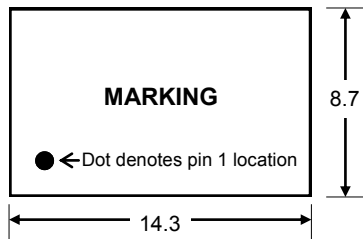
- HIGH FREQUENCY
- ULTRA LOW PHASE JITTER PERFORMANCE
- ENABLE / DISABLE



■ SPECIFICATION

| | | |
|-------------------------------------|---|---|
| FREQUENCY RANGE | | 0.750MHz ~ 800.000MHz |
| FREQUENCY STABILITY | | ±10ppm ~ ±100ppm |
| OPERATING TEMPERATURE RANGE | | STD: 0°C ~ 70°C, Option: -40°C ~ 85°C (See P/N guide for other options) |
| STORAGE TEMPERATURE RANGE | | -55°C ~ 125°C |
| SUPPLY VOLTAGE ± 5% | | 2.5V _{DC} ~ 5.0V _{DC} |
| SUPPLY CURRENT | | 100mA max. |
| OUTPUT | LOGIC FAMILY | LVDS |
| | LOAD | 100Ω (Output - Complementary Output) |
| | LEVEL LOGIC '1' (V_{OH}) | 1.4V _{DC} typ. |
| | LEVEL LOGIC '0' (V_{OL}) | 1.1V _{DC} typ. |
| | DIFFERENTIAL OUTPUT VOLTAGE | 330mV typ. |
| | OFFSET VOLTAGE | 1.2V typ. |
| | SYMMETRY(DUTY CYCLE) | STD: 40% / 60% , Option: 45% / 55% |
| STAND-BY VOLTAGE | ENABLE (V_{IH}) | 0.7 x V _{DD} ≤ V _{IH} ≤ V _{DD} or N/C |
| | DISABLE (V_{IL}) | V _{IL} ≤ 0.3 x V _{DD} |
| AGING @ +25°C ±3°C | | ±3.0ppm / 1 st year max. |
| PHASE JITTER (12kHz ~ 20MHz) | | 1.0pS max. |

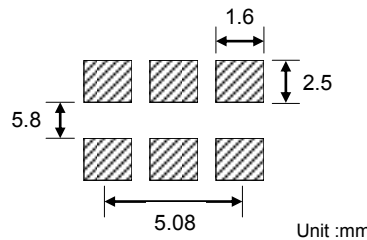
■ PACKAGE DIMENSIONS



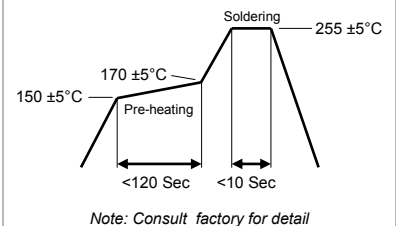
Unit : mm

| Pin Configuration | |
|-------------------|-----------------|
| 1 | N.C. or En/Dis |
| 2 | N.C. or En/Dis |
| 3 | GND |
| 4 | Output |
| 5 | C - output |
| 6 | V _{DD} |

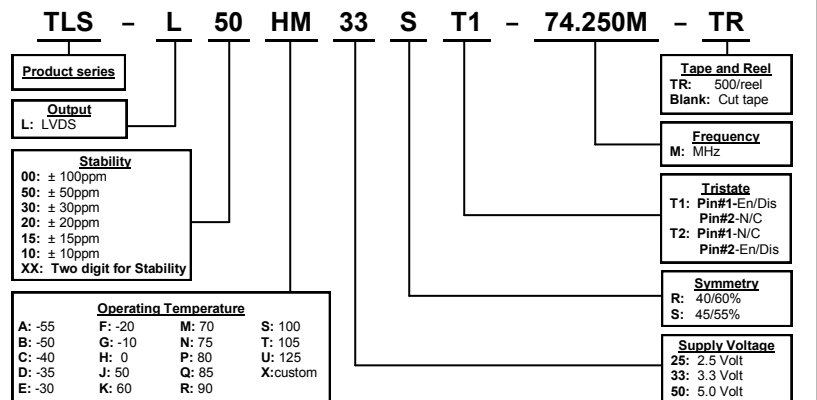
■ SOLDER PATTERN



■ REFLOW PROFILE



■ PART NUMBERING GUIDE



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